

PATENT

FLAT LAMP, MANUFACTURING PROCESS AND APPLICATION

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ABSTRACT

A flat lamp (1) comprising at least two glass substrates (2, 3) kept mutually parallel and defined in an internal gas-filled space (10), comprising two electrodes (4, 5) associated with the glass substrates and away from the internal space (10), in which the internal face (22, 32) of at least one substrate (2, 3) turned toward said internal space (10) is coated with a phosphor material (6, 7), characterized in that at least one of the electrodes (4, 5) is covered with at least one preferably transparent electrical insulation (2, 3; 14, 16; 15, 17) that may be formed by at least one of the glass substrates (2, 3) or be associated with at least one of the glass substrates (2, 3).

Figure 1